



**RIGHT ANGLE HEADER WITH
STANDARD LATCH FOR
MASTER-SLAVE LINK**

Header Pin	Pin Number
MSD1P	1
MSD1N	2
MSD2P	3
MSD2N	4
MSD3P	5
MSD3N	6
MSD4P	7
MSD4N	8
MSD5P	9
MSD5N	10
MSD6P	11
MSD6N	12
MSD7P	13
MSD7N	14
MSD8P	15
MSD8N	16
MSD9P	17
MSD9N	18
MSD10P	19
MSD10N	20
MSD11P	21
MSD11N	22
MSD12P	23
MSD12N	24
	25
	26

○ DIFFERENTIAL PAIR

Title		
FPGA IO BANKS 13 & 14		
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