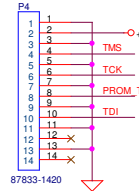


INSTALL ONLY ONE R67 OR R69

CSC TRIGGER PATH

BACKEND TRIGGER PATH

XILINX JTAG CONNECTOR



- SW3-1 ON: DISABLE GBT PATH
- SW3-2 ON: SPARE INPUT TO FPGA
- SW3-3 ON: CLK FROM GBT
- SW3-4 ON: GBT CONFIGSELECT TO 1.5V (DONGLE)

| | | | |
|---------------------|--------------------------|-------|---------|
| Title | | | |
| FPGA-PROM INTERFACE | | | |
| Size | Document Number | | Rev |
| B | <Doc> | | 1.0 |
| Date: | Friday, January 25, 2019 | Sheet | 8 of 11 |

○ DIFFERENTIAL PAIR