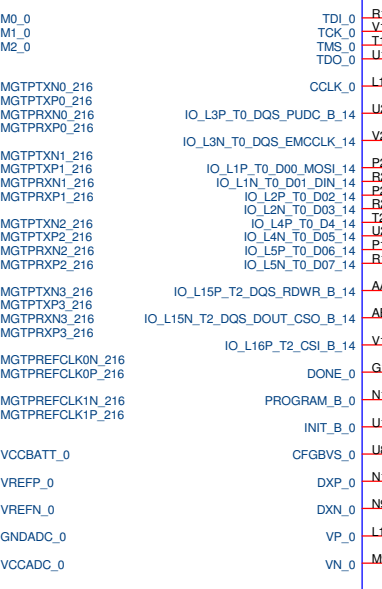


INSTALL R69 ONLY

U1A



XC7A75T-2FGG484C

VCCBATT_0

VREFP_0

VREFN_0

GNDADC_0

VCCADC_0

K9

K10

K11

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K17

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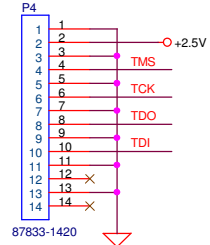
K37

K38

K39

K40

XILINX JTAG CONNECTOR



⊕ DIFFERENTIAL PAIR

Title		
FPGA CONFIGURATION		
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B	<Doc>	1.0
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